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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/314,750	05/19/1999	HIROSHI MURAKAMI	0941.63081	5601

24978 7590 05/02/2003

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EXAMINER

LESPERANCE, JEAN E

ART UNIT	PAPER NUMBER
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2674

DATE MAILED: 05/02/2003

18

Please find below and/or attached an Office communication concerning this application or proceeding.

B

Office Action Summary

Application No.

09/314,750

Applicant(s)

MURAKAMI, HIROSHI

Examiner

Jean E Lesperance

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 February 2003.
- 2a) ☒ This action is FINAL. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 2-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 2-11 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 May 1999 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

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DETAILED ACTION

Drawings

1. This application has been filed with informal drawings which are acceptable for examination purposes only. Formal drawings will be required when the application is allowed.
2. The Allowable subject matter of claims 2-7 is withdrawn from consideration.

Claim Rejections - 35 U.S.C. § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 2-11 are rejected under 35 U.S.C. 102 (e) as being unpatentable over U.S. Patent # 5,714,985 ("Kawamura et al.").

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As for claims 2-7, Kawamura et al. teach a display unit Fig.1 (6); video frame memories 1-1-1 and 1-1-2 (Fig.2) corresponding to memories which store information for controlling displaying of the data of the image on said display unit; multi-window controller Fig.2 (13) corresponding to an operation circuit unit which controls said display unit to display the data of the image supplied through said display data line; an image bus 8 exclusively for image processing (column 2, lines 46-47) corresponding to a data bus which connects said memories to an exterior of said display device and a display data line which supplies data of the image from an exterior to said display unit; an address bus (abstract) corresponding to an address bus which connects said memories to the exterior of said display device, a VME bus, image bus, and serial bus in which include a gate driver and a data driver which operates upon the information from image memories 2-1 and 2-2 (column 2, lines 35-44) corresponding to a data driver and a gate driver, wherein at least one of said gate driver and said data driver operate based on the information stored in said memories.

As for claim 8, Kawamura et al. teach a CPU board Fig.3 (21) corresponding to a display-information acquisition circuit which acquires information about said display unit; and video frame memories 1-1-1 and 1-1-2 (Fig.2) corresponding to display-information memories which store the information about said display unit, an image bus 8 exclusively for image processing (column 2, lines 46-47) corresponding to a data bus which connects said memories to an exterior of said display device and a display data line which supplies data of the image from an exterior to said display unit; an address bus (abstract) corresponding to an address bus which connects said

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memories to the exterior of said display device.

As for claim 9, Kawamura et al. teach a CPU board Fig.3 (21) corresponding to a display-information acquisition circuit checks said display unit to acquire information about said display unit with regard to a defect of said display unit.

As for claims 10 and 11, Kawamura et al. teach a slave video frame memory 1-1 sets an arbitrary position (Sx, Sy) as the end point of the rectangular area (column 8, lines 48-50) corresponding to said display unit with regard to coordinates of a position at which input is entered on said display unit; a plurality of pixel electrodes corresponding to the respective polysilicon thin-film transistors is inherent in the art.

Response to Amendment

4. Applicant's arguments filed 2-11-2003 have been fully considered but they are not persuasive. The applicant argued that the prior art does not teach memories which store information for controlling displaying of the data of the image on said display unit, said information being different from said data of the image. Examiner disagrees because the prior art teaches the address calculation unit 4 shown in FIG. 1 is connected to the VEM bus 7 and the IMAGE bus 8 in the case of random input and output, and effects high-speed pipeline processing such as image enlargement, reduction, displacement or rotation (column 3, lines 8-12) and 2-1 and 2-2 are different from the memory that runs all the programs. The applicant has to amend the

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claims to overcome the prior art because there is not a clear difference between them. Therefore the rejection is maintained as was rejected in the previous office action.

Conclusion

5. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean Lesperance whose telephone number is (703) 308-6413. The examiner can normally be reached on from Monday to Friday between 8:00AM and 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe, can be reached on (703) 305-4709 .

Any response to this action should be mailed to:

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Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal drive,
Arlington, VA, Sixth Floor (Receptionist).


Any inquiry of a general nature or relating to the status of this application or proceeding
should be directed to the technology Center 2600 Customer Service Office whose telephone
number is (703) 306-0377.

Jean Lesperance



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Date 4-22-2003



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600